

17
18
H
I
A
B
C
D
D
C
B
A

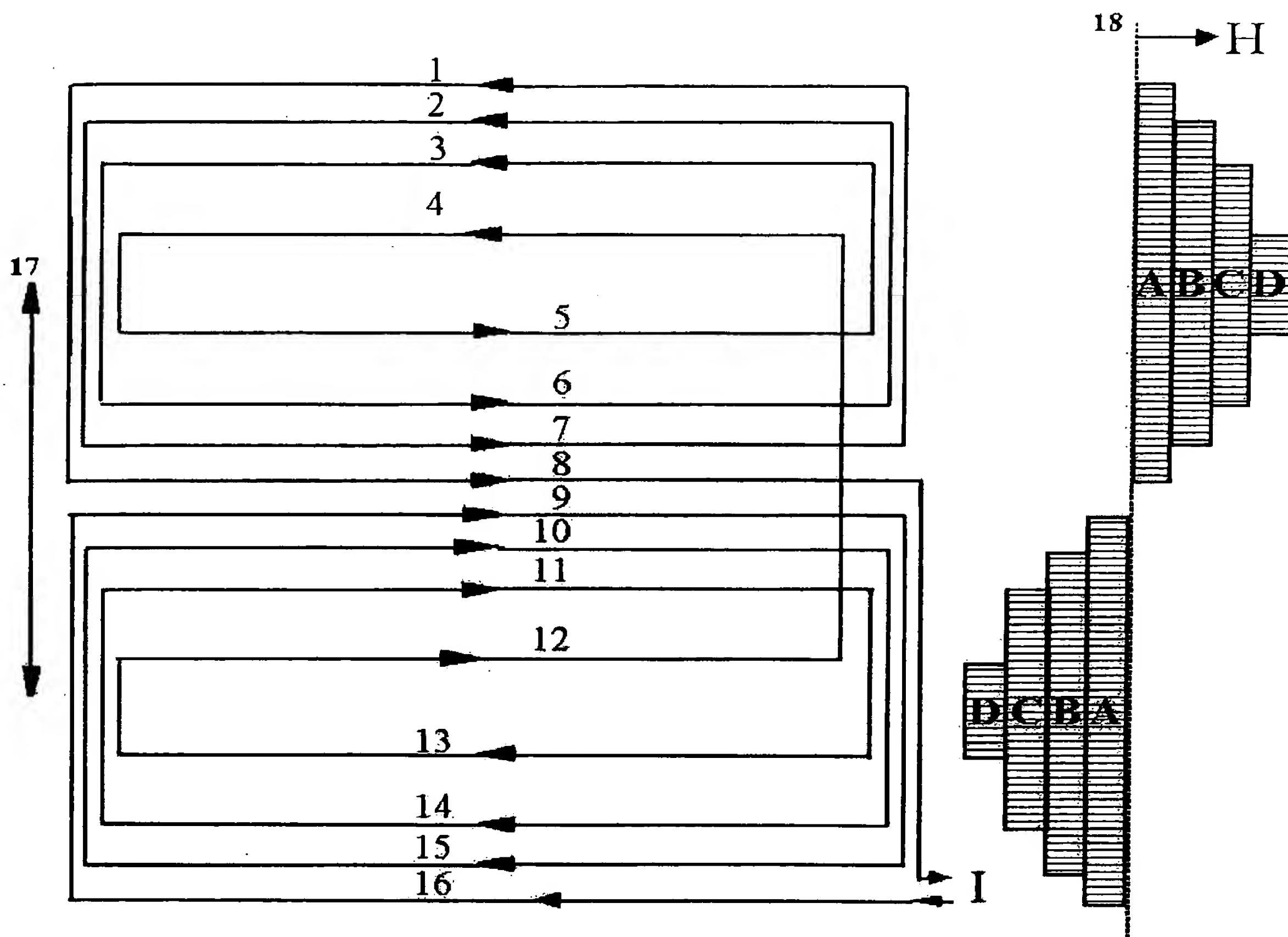


Fig. 1

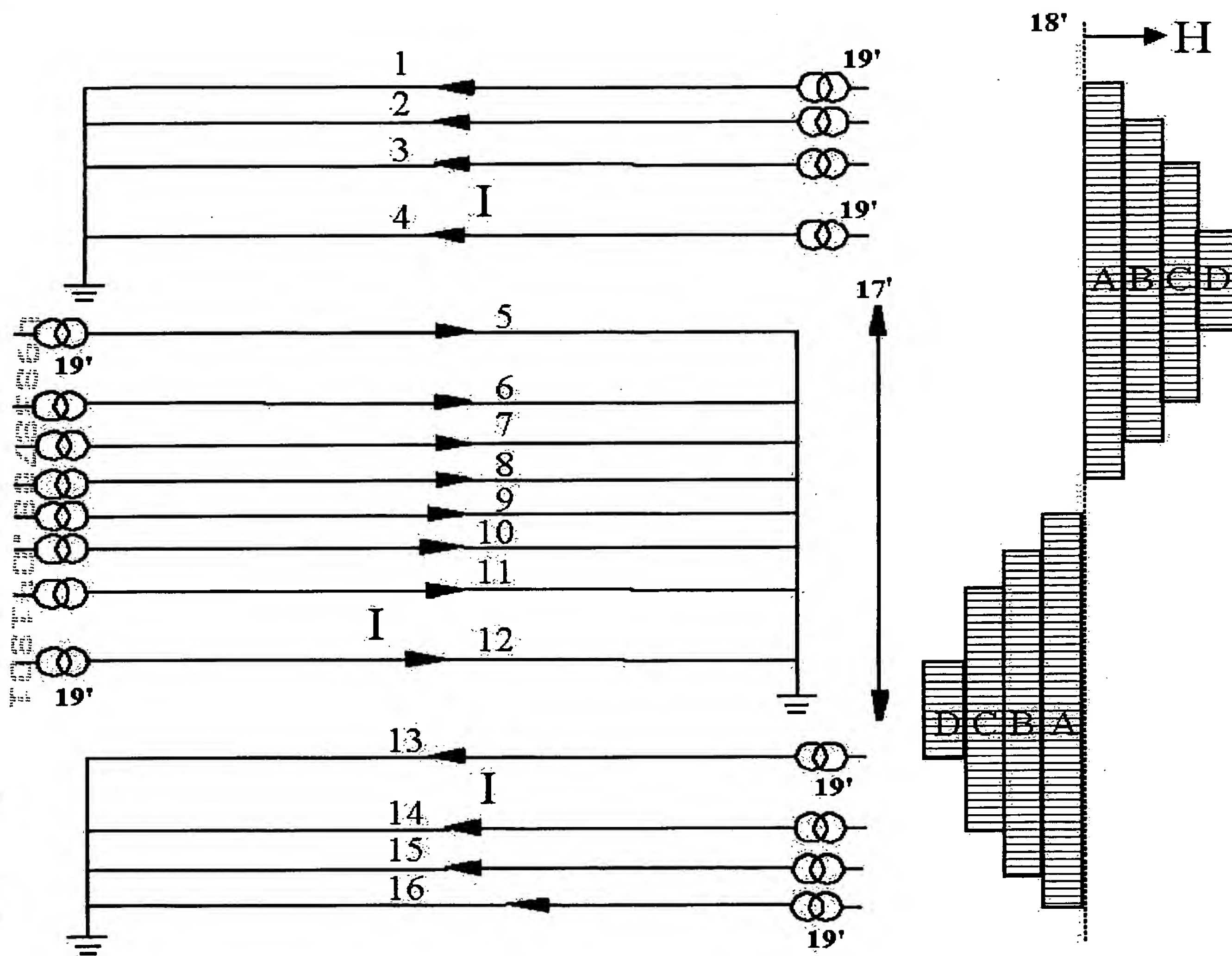


Fig. 2

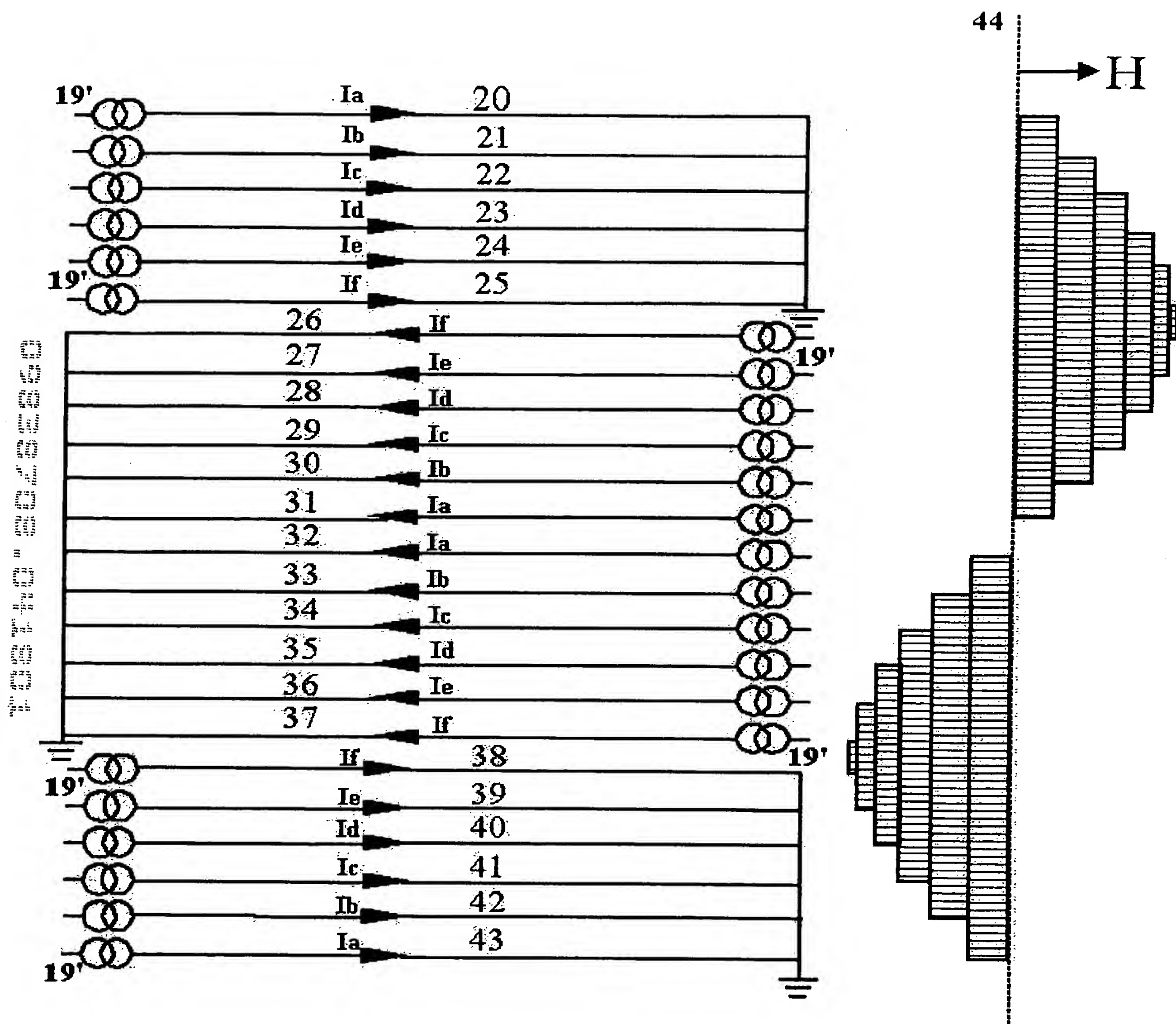


Fig. 3

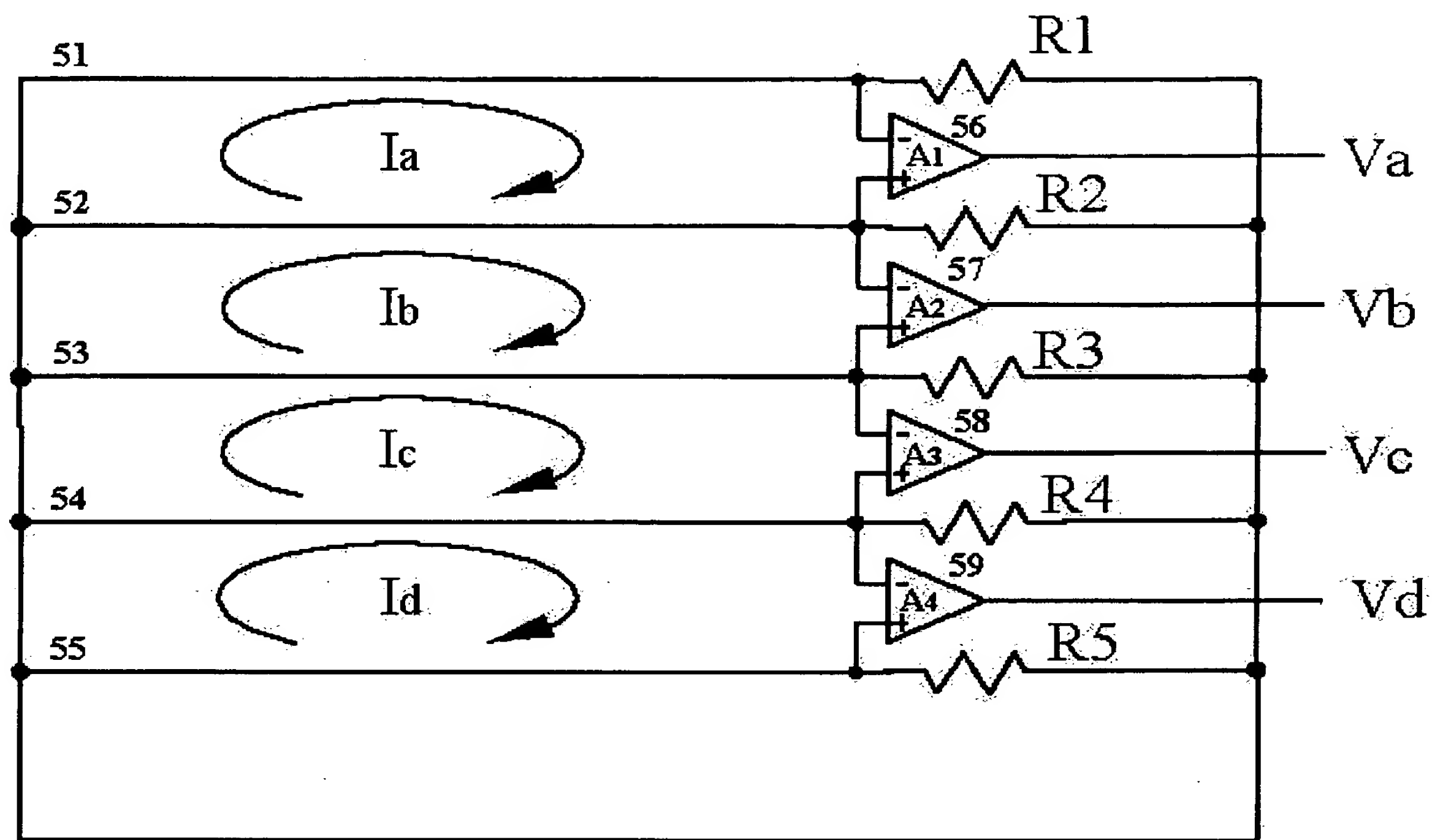


Fig. 4

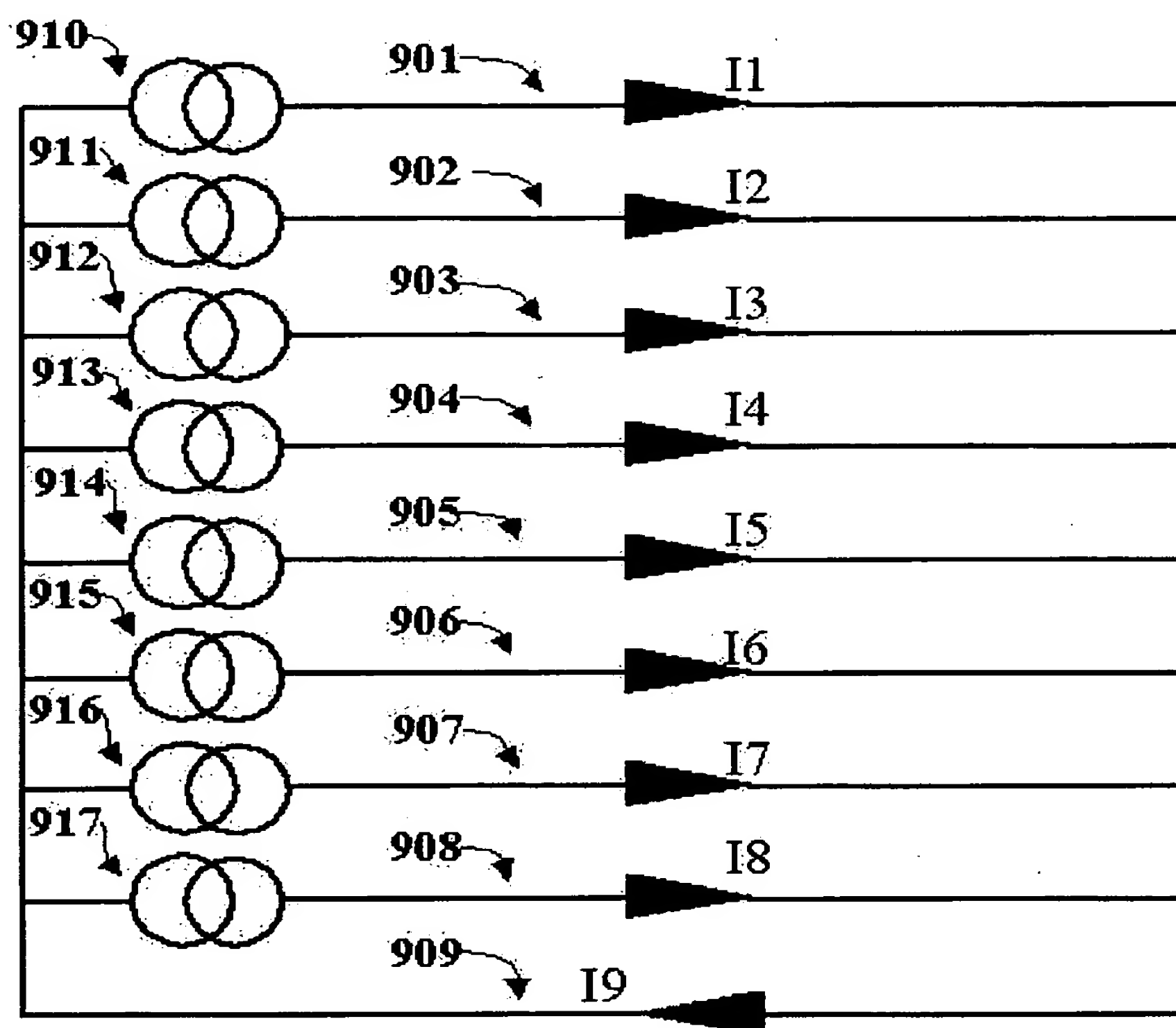


FIG. 5

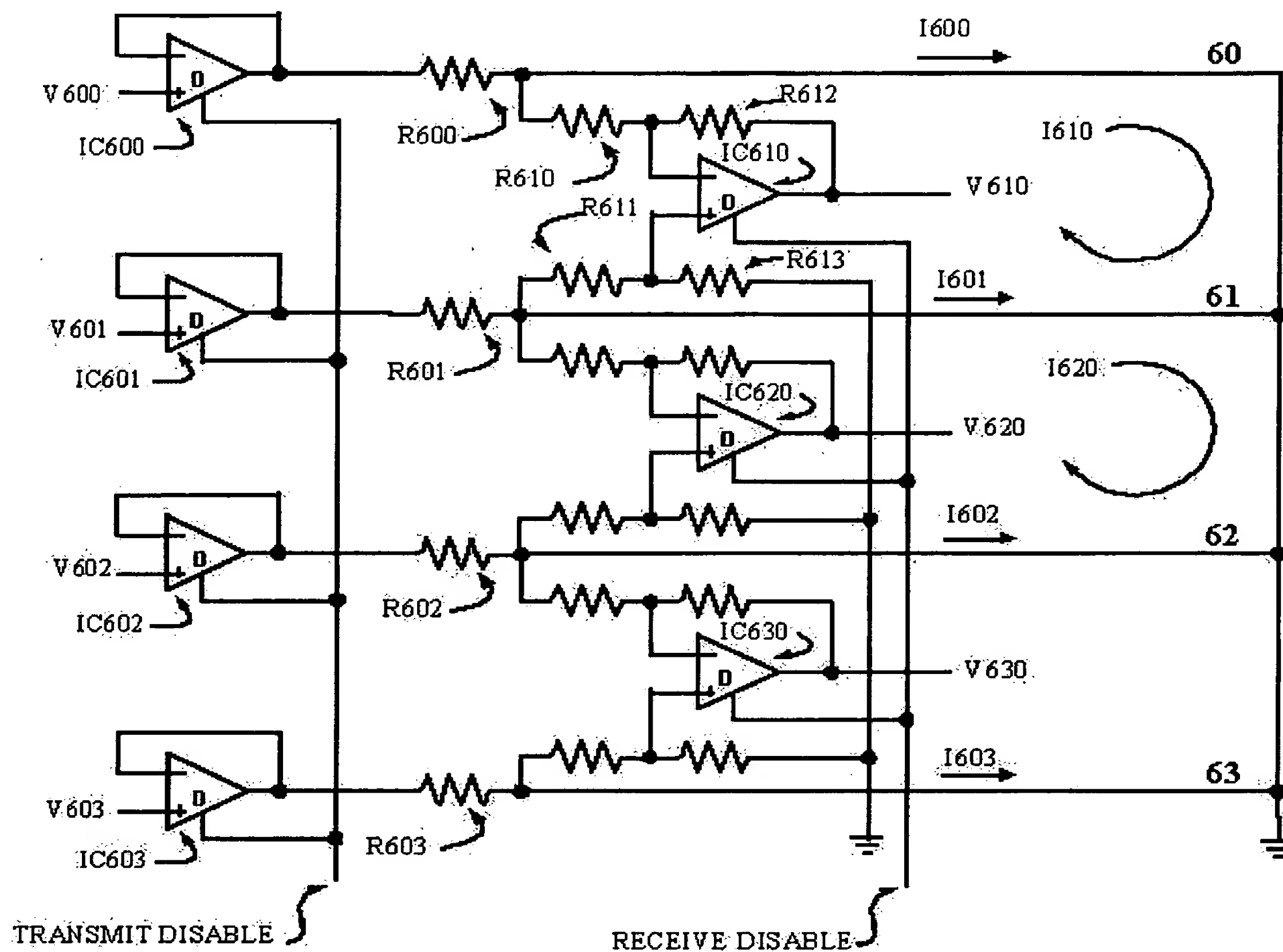


FIG. 6

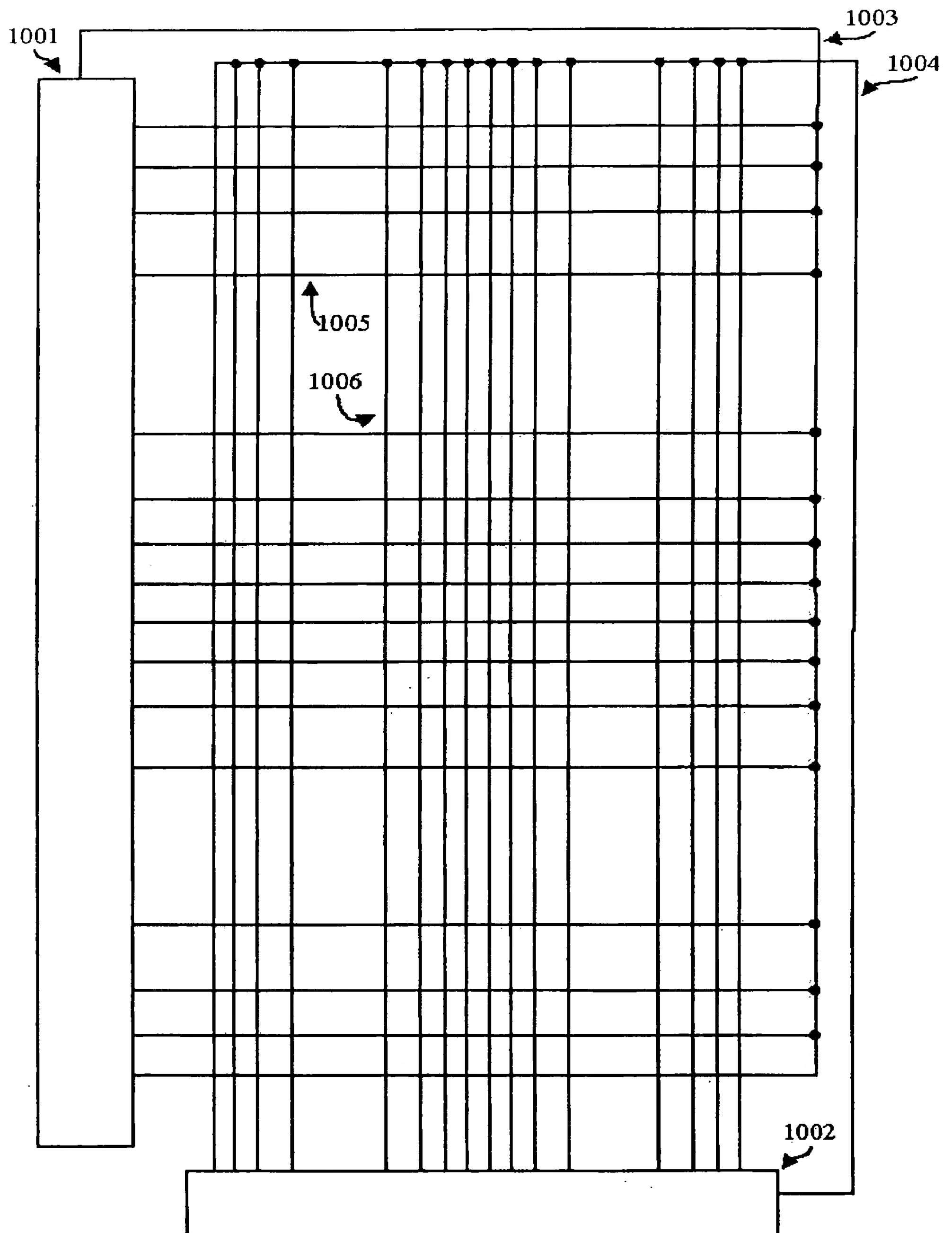


FIG. 8

FIG. 9 is a schematic diagram of a device 1101. The device 1101 includes a substrate 1102, a first layer 1104, a second layer 1105, and a third layer 1103. The first layer 1104 is disposed on the substrate 1102, and the second layer 1105 is disposed on the first layer 1104. The third layer 1103 is disposed on the second layer 1105. The first layer 1104 and the second layer 1105 are disposed in a central region of the device 1101, and the third layer 1103 is disposed in a peripheral region of the device 1101.

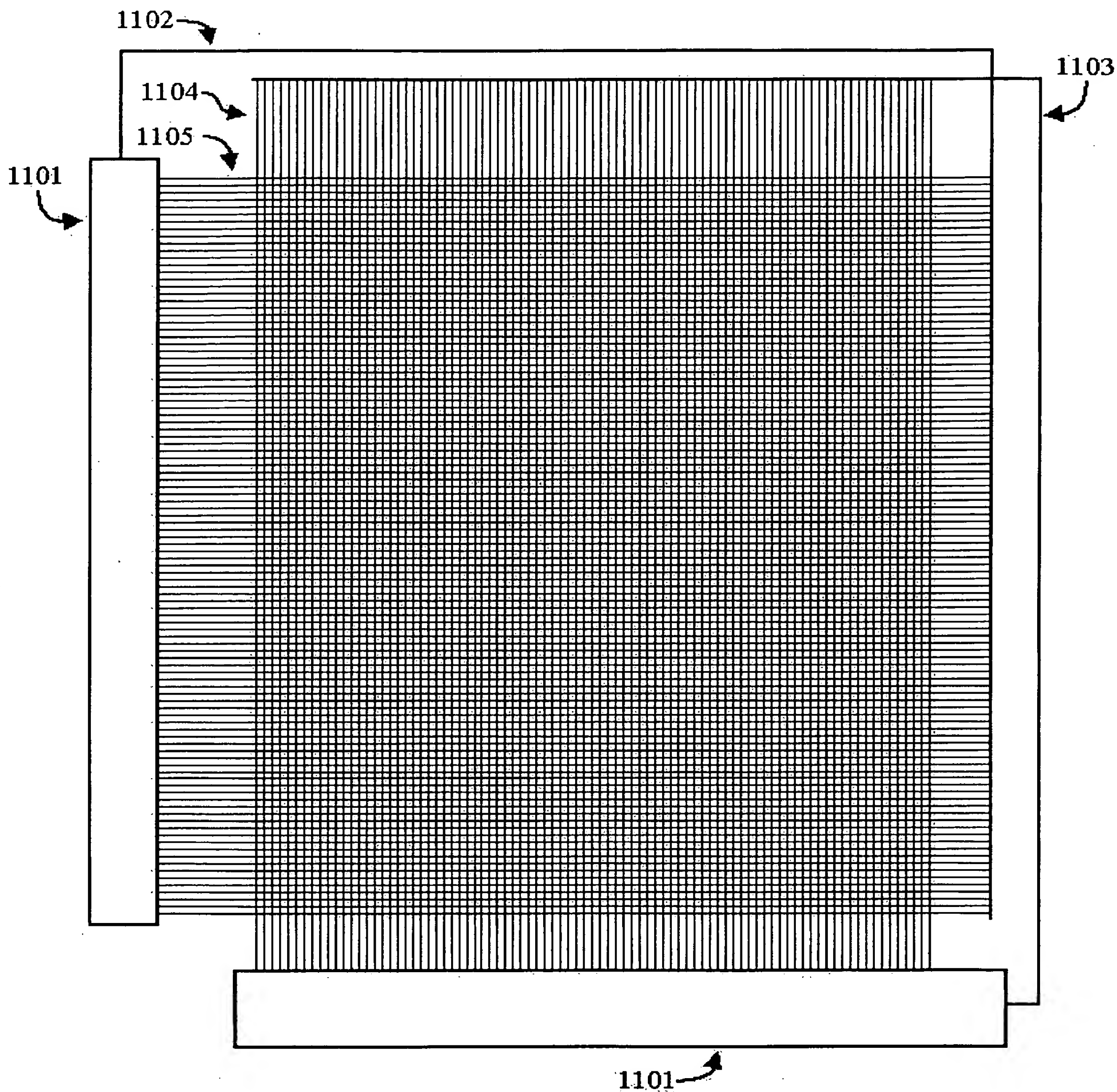


FIG. 9

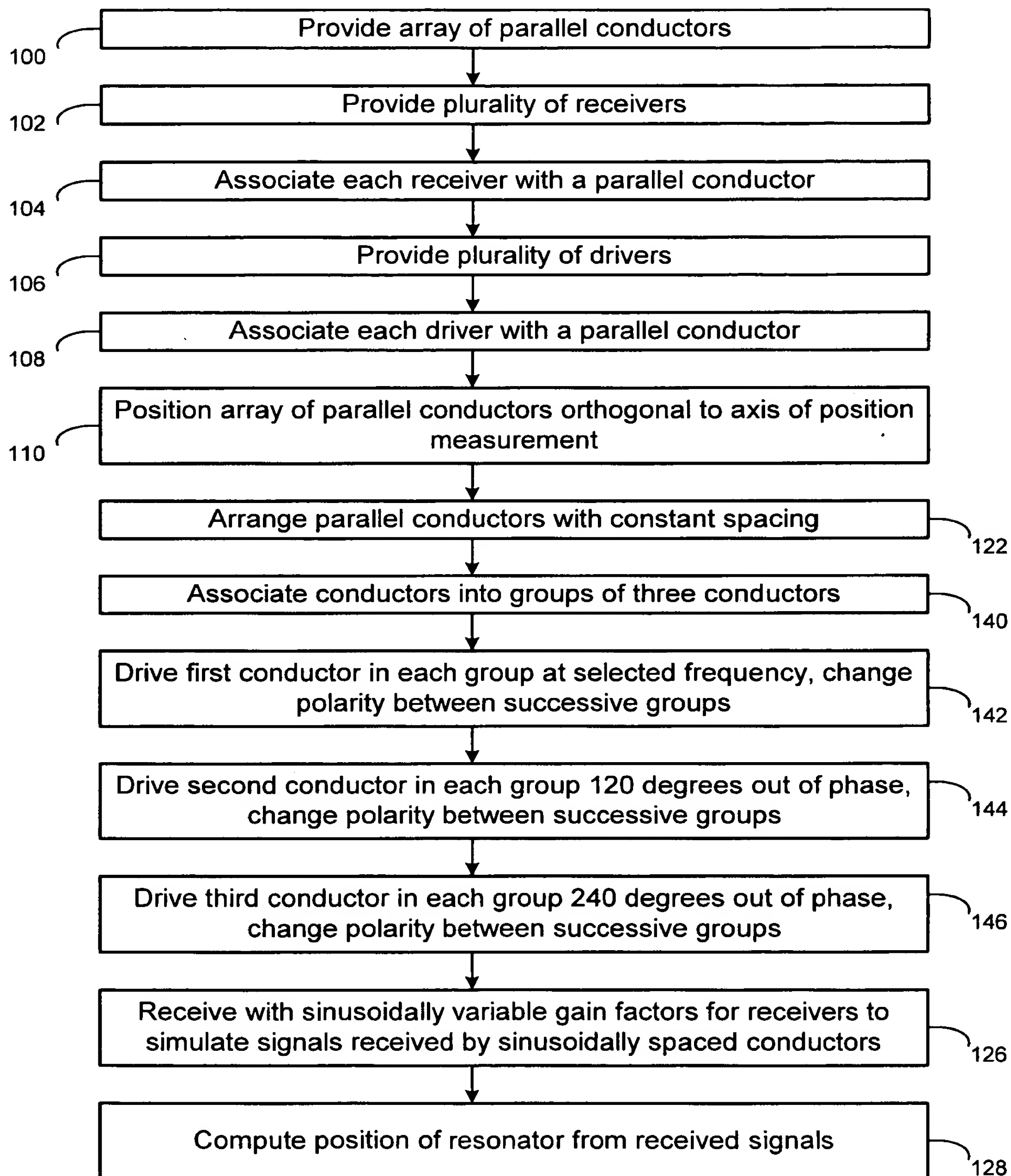


FIG. 11

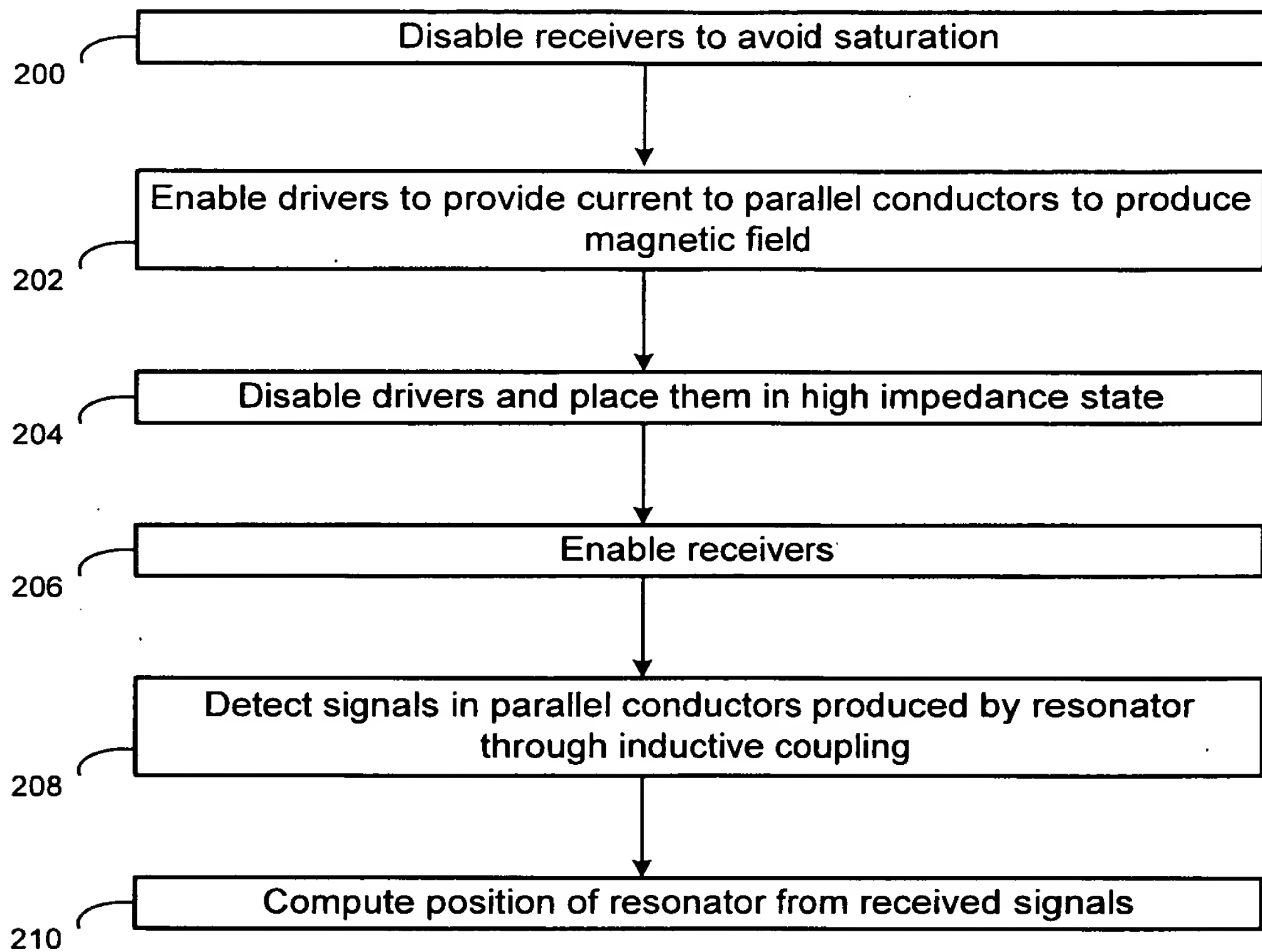


FIG. 12